

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Eliyahou Harari et al.  
Title: Flash EEprom System

Application No.: 09/867,836 Filing Date: May 30, 2001  
Examiner: Tran, Andrew Q. Group Art Unit: 2824  
Docket No.: SNDK.A06US7 Conf. No.: 6307

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 25, 2006

Mary E. Buggie  
Mary E. Buggie

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants call the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

According to 37 C.F.R. 1.98(2)(ii), copies of the U.S. Patents and U.S. Published Patent Applications documents are not required and are therefore not enclosed. Copies of the listed foreign patent documents and Other Art are enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a

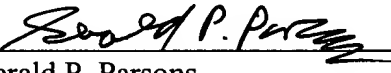
Attorney Docket No.: SNDK.A06US7

Application No.: 09/867,836

representation that a search has been made (other than as indicated by any cited document), or  
(3) an admission that the cited information is, or is considered to be, material to patentability as  
defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and  
consequently no fee should be required. The Commissioner is authorized, however, to charge  
any fee that may be required, or to credit any overpayment, against Deposit Account No.  
502664. This form is being submitted in duplicate.

Respectfully submitted,

  
\_\_\_\_\_  
Gerald P. Parsons  
Reg. No. 24,486

January 25, 2006  
\_\_\_\_\_  
Date

PARSONS HSUE & DE RUNTZ LLP  
595 Market Street, Suite 1900  
San Francisco, CA 94105  
(415) 318-1160 (main)  
(415) 318-1163 (direct)  
(415) 693-0194 (fax)

U.S. Department of Commerce, Patent and Trademark				Atty. Docket No.		Application No.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)  (Form PTO-1449)				SNDK.A06US7		09/867,836	
				Applicants		Conf. No.	
				Eliyahou Harari et al.		6307	
				Filing Date		Art Group	
				May 30, 2001		2824	

U.S. Patent Documents							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	4,090,258	5/16/78	Cricchi			
	2	4,677,590	6/30/87	Arakawa			
	3	4,691,298	9/1/87	Fukuda et al.			
	4	4,737,936	4/12/88	Takeuchi			
	5	4,939,690	7/3/90	Momodomi et al.			
	6	4,943,962	7/24/90	Imamiya et al.			
	7	4,959,812	9/25/90	Momodomi et al.			
	8	4,980,861	12/25/90	Herd et al.			
	9	4,996,669	2/26/91	Endoh et al.			
	10	5,034,922	7/23/91	Burgess			
	11	5,172,338 B1	7/8/97	Mehrotra et al.			
	12	5,357,462	10/18/94	Tanaka et al.			
	13	5,361,227	11/1/94	Tanaka et al.			
	14	5,657,270	8/12/97	Ohuchi et al.			
	15	5,793,696	8/11/98	Tanaka et al.			
	16	5,831,903	11/3/98	Ohuchi et al.			

Foreign Patent Documents							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	17	JP62257699	10/11/87	Japan			Abstract	X
	18	DE 3831538 A1	30/3/89	Germany			Abstract	X
	19	DE3637682 A1	14/5/87	Germany			Abstract	X

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
20	Kynett, Virgil Niles et al., "An In-System Reprogrammable 32K x 8 CMOS Flash Memory," IEEE Journal of Solid-State Circuits, Vol. 23, No. 5, October 1998, 7 pages.	
21	SEEQ Data Book 1988/89, <u>SEEQ Technology, Inc.</u> , pp. 3-1-3-7.	
22	USPTO, Office Action mailed in Application No. 09/635,597 on August 22, 2005, 12 pages.	

U.S. Department of Commerce, Patent and Trademark		Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		SNDK.A06US7	09/867,836
		Applicants	Conf. No.
(Use several sheets if necessary)		Eliyahou Harari et al.	6307
(Form PTO-1449)		Filing Date	Art Group
		May 30, 2001	2824
23	"Bipolar/MOS Memories Data Book," <u>Advanced Micro Devices</u> , ISSN 0888-4714, 1986, pp. 6-1-6-14.		
24	Kynett, Virgil Niles et al., "A 90-ns One-Million Erase/Program Cycle 1-Mbit Flash Memory," IEEE Journal of Solid-State Circuits, Vol. 24, No. 5, October 1989, pp. 1259-1264.		
25	IBM Technical Disclosure Bulletin, "EPROM Programming Device," Vol. 29, No. 9, February 1987, pp. 4145-4146.		
26	Tanaka, Tomoharu et al., "A 4-Mbit NAND-EEPROM with Tight Programmed Vt Distribution," 1990 Symposium on VLSI Circuits, Digest of Technical Papers, pp. 105-106.		
27	"An Analysis of SGS Thomson M293B1 EPM32: Electronic Program Memory for 32 Stations", <u>Chipworks</u> , October 19, 1996.		
28	"SGS M293 Electronic Program Memory Device", <u>Taeus</u> Report 60723, August 9, 1996.		
29	"Initial Determination, U.S. International Trade Commission, Investigation No. 337-TA-382, March 25, 1997.		
30	Japanese Patent Office Appeal/Trial Decision in Japanese Patent Application No. 2-506478, October 23, 2001.		
Examiner		Date Considered	
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>			